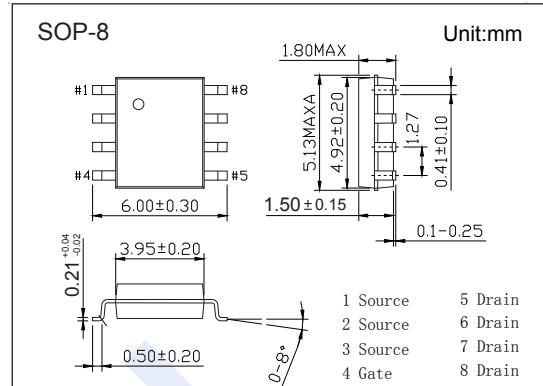
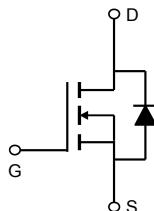


N-Channel MOSFET

AO4482 (KO4482)

■ Features

- $V_{DS} (V) = 100V$
- $I_D = 6 A (V_{GS} = 10V)$
- $R_{DS(ON)} < 37m\Omega (V_{GS} = 10V)$
- $R_{DS(ON)} < 42m\Omega (V_{GS} = 4.5V)$



■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current	I_D	6	A
		5	
Pulsed Drain Current	I_{DM}	42	
Avalanche Current	I_{AS}, I_{AR}	35	
Avalanche Energy	E_{AS}, E_{AR}	61	mJ
Power Dissipation	P_D	3.1	W
		2	
Thermal Resistance.Junction- to-Ambient	R_{thJA}	40	°C/W
		75	
Thermal Resistance.Junction- to-Lead	R_{thJL}	24	
Junction Temperature	T_J	150	
Storage Temperature Range	T_{stg}	-55 to 150	°C

N-Channel MOSFET

AO4482 (KO4482)

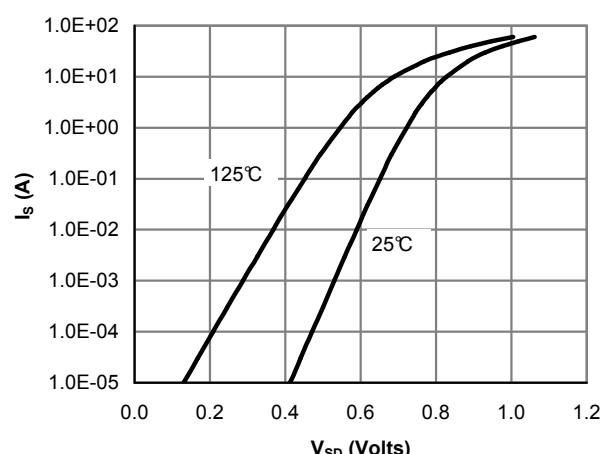
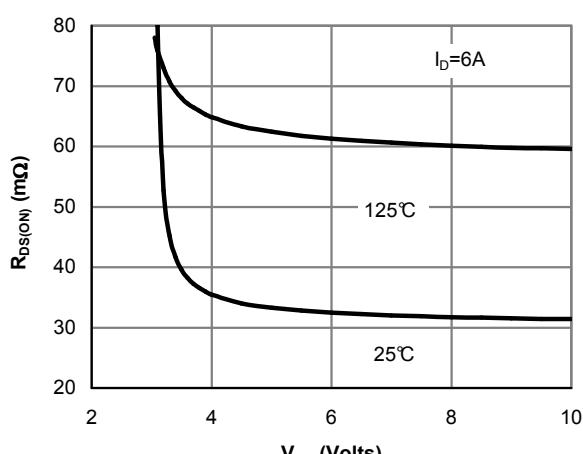
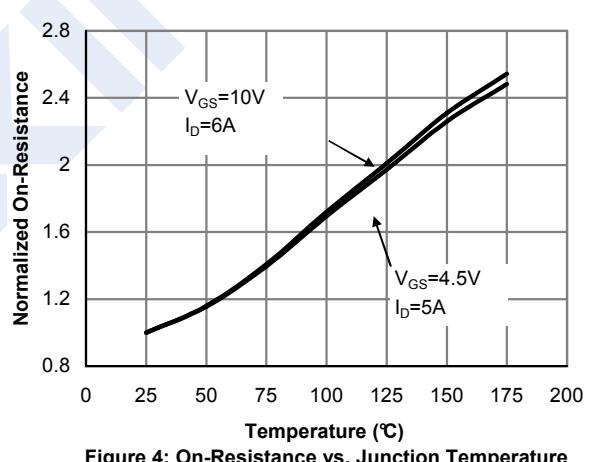
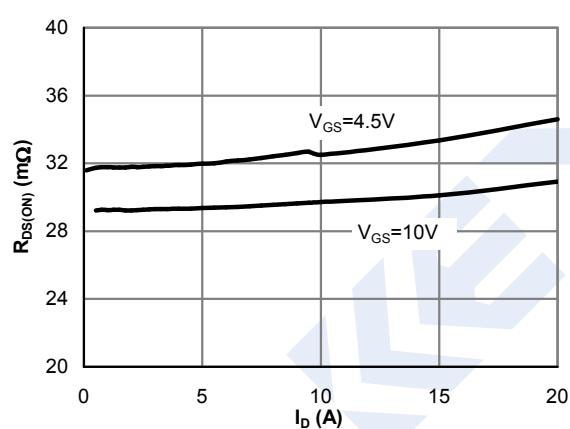
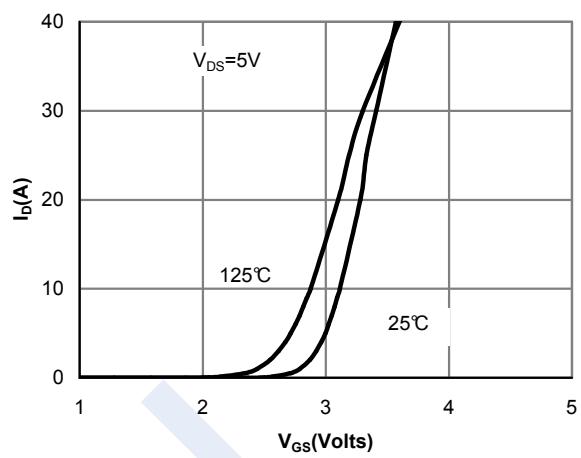
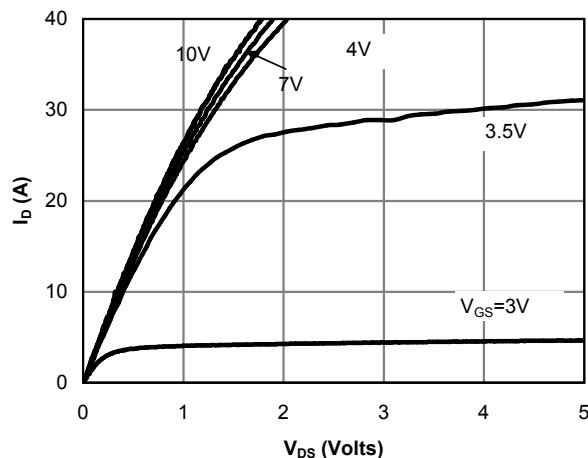
■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V _{DSS}	I _D =250 μ A, V _{GS} =0V	100			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DSS} =100V, V _{GS} =0V		1		uA
		V _{DSS} =100V, V _{GS} =0V, T _J =55°C		5		
Gate-Body Leakage Current	I _{GSS}	V _{DSS} =0V, V _{GS} =±20V			±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DSS} =V _{GS} , I _D =250uA	1.6		2.7	V
Static Drain-Source On-Resistance	R _{Ds(on)}	V _{GS} =10V, I _D =6A		37		mΩ
		V _{GS} =10V, I _D =6A T _J =125°C		72		
		V _{GS} =4.5V, I _D =5A		42		
On State Drain Current	I _{D(on)}	V _{GS} =10V, V _{DSS} =5V	42			A
Forward Transconductance	g _{FS}	V _{DSS} =5V, I _D =6A		35		S
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DSS} =50V, f=1MHz	1300		2000	pF
Output Capacitance	C _{oss}		70		130	
Reverse Transfer Capacitance	C _{rss}		30		70	
Gate Resistance	R _g	V _{GS} =0V, V _{DSS} =0V, f=1MHz	0.3		1.1	Ω
Total Gate Charge (10V)	Q _g	V _{GS} =10V, V _{DSS} =50V, I _D =6A	28		44	nC
Total Gate Charge (4.5V)	Q _{gs}		14		22	
Gate Source Charge	Q _{gd}		4		6	
Gate Drain Charge	Q _{gd}		5		13	
Turn-On DelayTime	t _{d(on)}	V _{GS} =10V, V _{DSS} =50V, R _L =8.3Ω, R _{GEN} =3Ω		7		ns
Turn-On Rise Time	t _r			7		
Turn-Off DelayTime	t _{d(off)}			28		
Turn-Off Fall Time	t _f			7		
Body Diode Reverse Recovery Time	t _{rr}	I _F = 6A, dI/dt= 500A/us	17.5		33	nC
Body Diode Reverse Recovery Charge	Q _{rr}		90		170	
Maximum Body-Diode Continuous Current	I _s				4	A
Diode Forward Voltage	V _{SD}	I _s =1A, V _{GS} =0V			1	V

Note : The static characteristics in Figures 1 to 6 are obtained using <300 us pulses, duty cycle 0.5% max.

■ Marking

Marking	4482 KC***
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N-Channel MOSFET**AO4482 (KO4482)****■ Typical Characteristics**

N-Channel MOSFET

AO4482 (KO4482)

■ Typical Characteristics

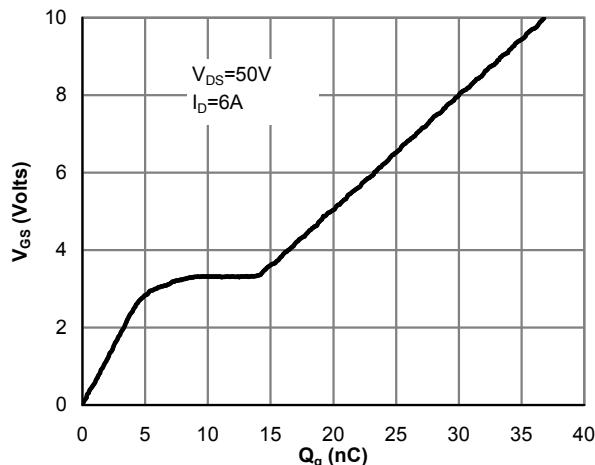


Figure 7: Gate-Charge Characteristics

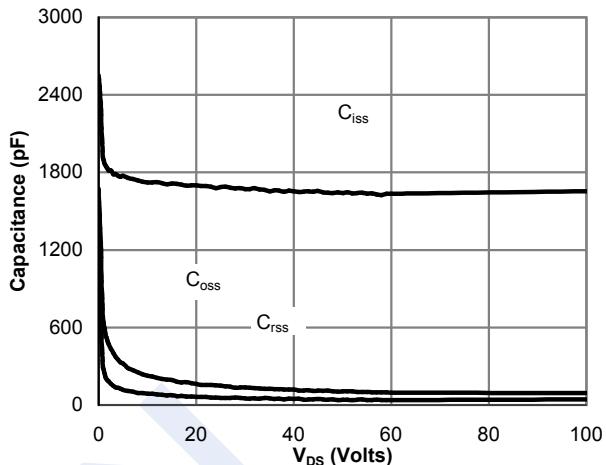


Figure 8: Capacitance Characteristics

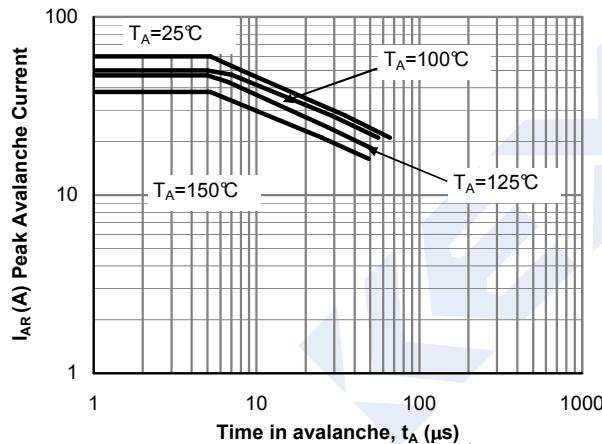


Figure 9: Single Pulse Avalanche capability (Note C)

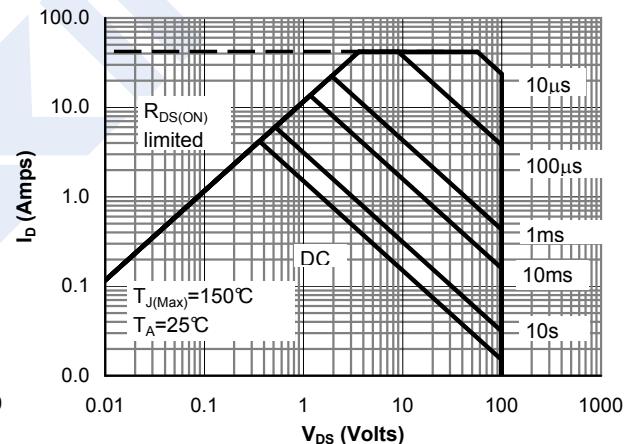


Figure 10: Maximum Forward Biased Safe Operating Area (Note F)

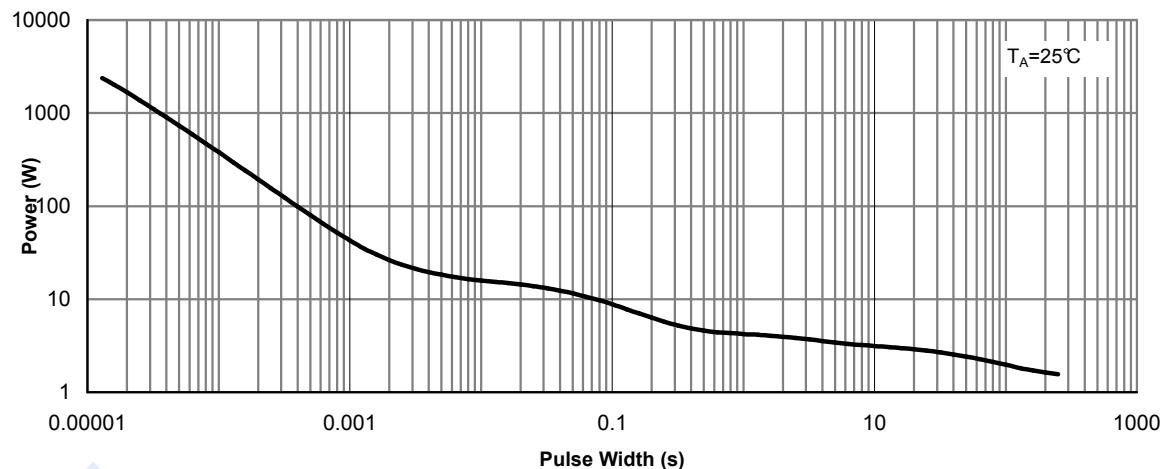


Figure 11: Single Pulse Power Rating Junction-to-Ambient (Note F)

N-Channel MOSFET**AO4482 (KO4482)**

■ Typical Characteristics

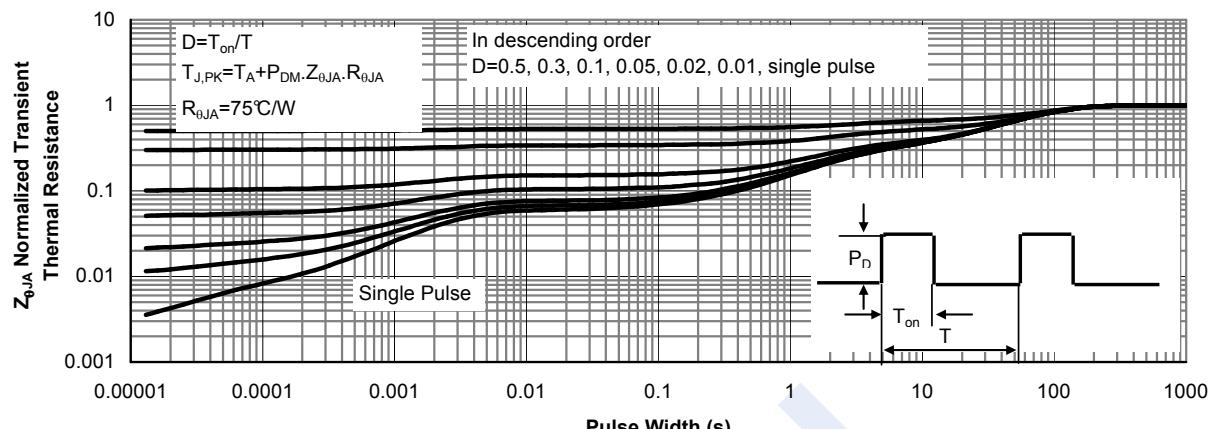


Figure 12: Normalized Maximum Transient Thermal Impedance (Note F)